

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application.

**In the Claims:**

1. (Currently Amended) A circuit sheet, comprising:  
a substrate;  
a first set of ridges formed in a first direction on the substrate, at least one of the ridges having a first portion of a first height and having a second portion of a second height;  
a second set of ridges formed in a second direction on the substrate; and  
wells disposed on the substrate, defined by respective intersections of the first and second sets of ridges, and wells disposed on the substrate and operable to hold, in a liquid phase, respective conductive polymers that when in a solid phase  
form circuit devices that can be interconnected to form an electronic circuit.
  
2. (Currently Amended) The sheet of claim 1, ~~f~~ ~~wherein~~ ~~further~~ comprising:  
~~a first set of ridges formed in a first direction on the substrate;~~  
~~a second set of ridges formed in a second direction on the substrate, the second direction is being substantially perpendicular to the first direction; and~~  
~~wherein the wells are defined by respective intersections of the first and second sets of ridges.~~
  
3. (Original) The sheet of claim 1 wherein the substrate is flexible.
  
4. (Currently Amended) A circuit sheet, comprising:  
a substrate having at least one surface region;  
conductive-polymer dots disposed on the at least one surface region of the substrate and having respective sizes; and

a chemical treatment disposed on the at least one surface regions of the substrate beneath the dots and operable to limit the sizes of the conductive-polymer dots when the dots are in a liquid phase printed onto the regions.

5. (Currently Amended) An electronic apparatus, comprising:  
a substrate;  
groups of conductive polymer dots disposed on the substrate in predetermined locations, the conductive polymer dots within each group interconnected to form a respective electronic device; and  
a connection layer that interconnects the electronic devices dots to form an electronic -circuit.

6. (Previously Presented) The apparatus of claim 5, further comprising a display disposed on the connection layer and operable to be driven by the circuit.

7. (Currently Amended) The apparatus of claim 5 wherein at least one of the conductive polymer dots comprises polymer poly-paraphenylene vinylene (PPV) poly-paraphenylene (PPP).

8. (Currently Amended) The apparatus of claim 5, further comprising wells formed disposed on the substrate in the predetermined locations and holding the dots.

9. (Currently Amended) The apparatus of claim 5 wherein the predetermined locations of the substrate are chemically treated to limit the respective sizes of the dots.

10. (Currently Amended) A circuit sheet, comprising:  
one and only one substrate; and  
transistors disposed on the substrate, and formed from a conductive polymer, wherein the transistors are electrically isolated from one another, and are operable to be interconnected to form an electronic circuit.

11. Cancelled.
12. (Previously Presented) A circuit, comprising:  
one and only one substrate;  
transistors disposed on the substrate and formed from a conductive polymer;  
and  
conductive traces disposed on the substrate and interconnecting the transistors in a predetermined topology to form an electronic circuit.
13. (Previously Presented) The circuit of claim 12, further comprising a display disposed on the substrate and operable to be driven by the interconnected transistors.
14. – 21. Cancelled.
22. (Previously Presented) The circuit sheet of claim 1, wherein the circuit devices comprise transistors.
23. (Currently Amended) The circuit sheet of claim 4, wherein the chemical treatment smoothens the a-surface region of the substrate.
24. (Previously Presented) The circuit sheet of claim 4, wherein the chemical treatment comprises a wax.
25. (Currently Amended) The apparatus of claim 5, wherein at least one of the electronic devices comprises, further comprising groups of conductive polymer dots disposed on the substrate in predetermined locations, each group comprising a respective transistor.
26. (Currently Amended) The apparatus of claim 58, wherein the wells also hold nonconductive polymer dots.

27. (Previously Presented) The circuit of claim 12, wherein the conductive traces are formed from a conductive polymer.

28. (New) A circuit sheet, comprising:  
a substrate;  
a first set of ridges formed in a first direction on the substrate, at least one of the ridges having a first height and at least another one of the ridges having a second height;  
a second set of ridges formed in a second direction on the substrate; and  
wells disposed on the substrate, defined by respective intersections of the first and second sets of ridges, and operable to hold respective conductive polymers that form circuit devices that can be interconnected to form an electronic circuit.

29. (New) A circuit sheet, comprising:  
a substrate;  
a first set of ridges formed in a first direction on the substrate;  
a second set of ridges formed in a second direction on the substrate, at least one ridge in the second set having a height that is different than a height of a ridge in the first set; and  
wells disposed on the substrate, defined by respective intersections of the first and second sets of ridges, and operable to hold respective conductive polymers that form circuit devices that can be interconnected to form an electronic circuit.

30. (New) The apparatus of claim 5 wherein at least one of the conductive polymer dots comprises poly-paraphenylene (PPP).